FIG. 1

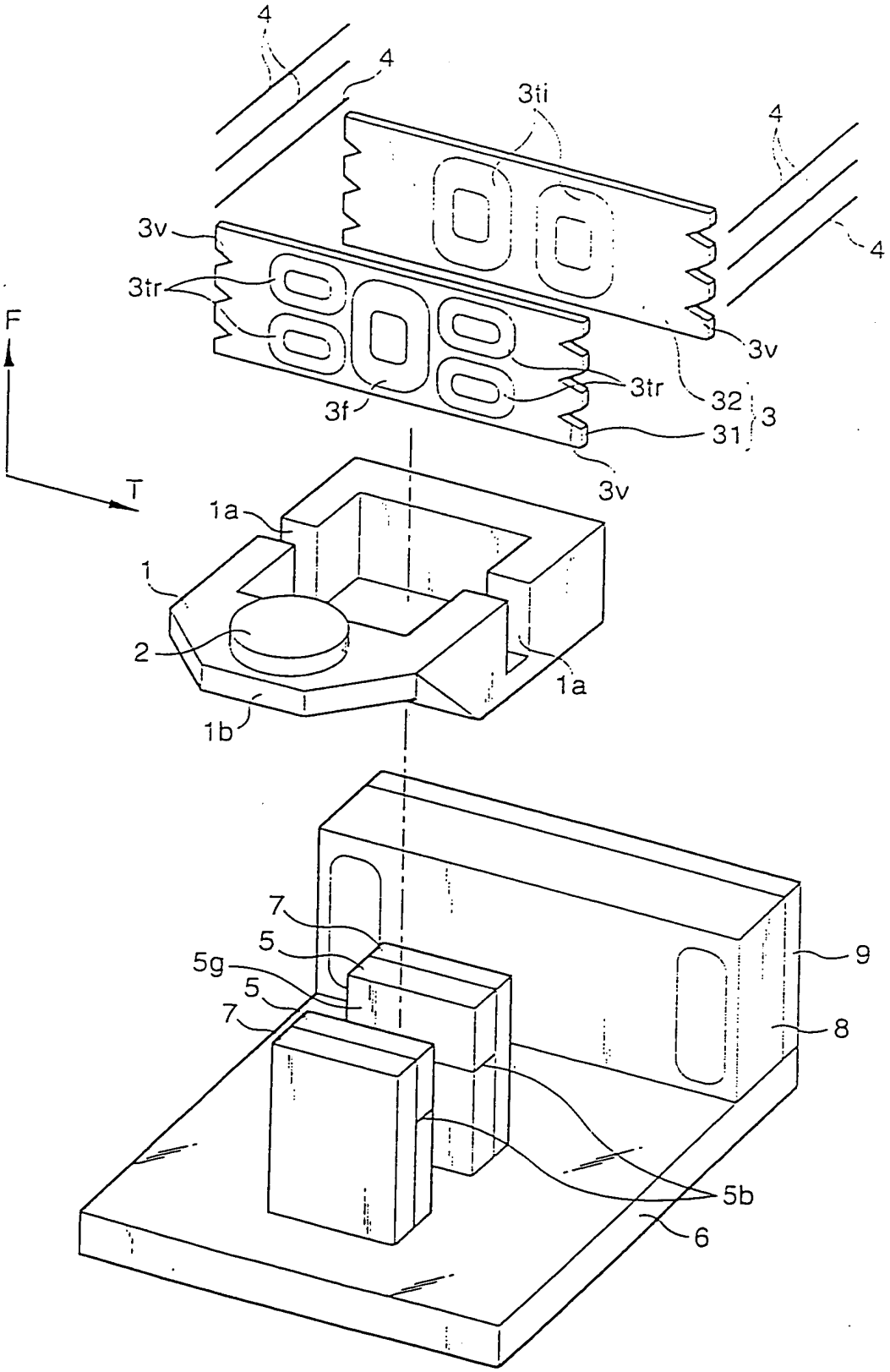


FIG. 2

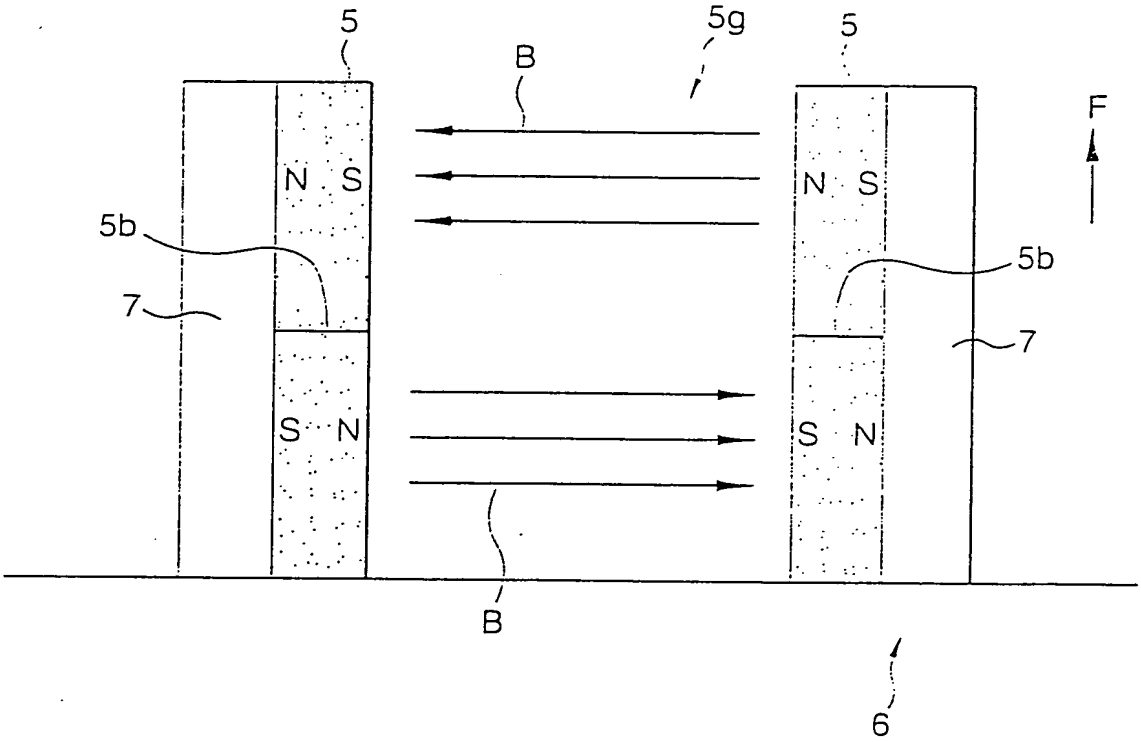


FIG. 3

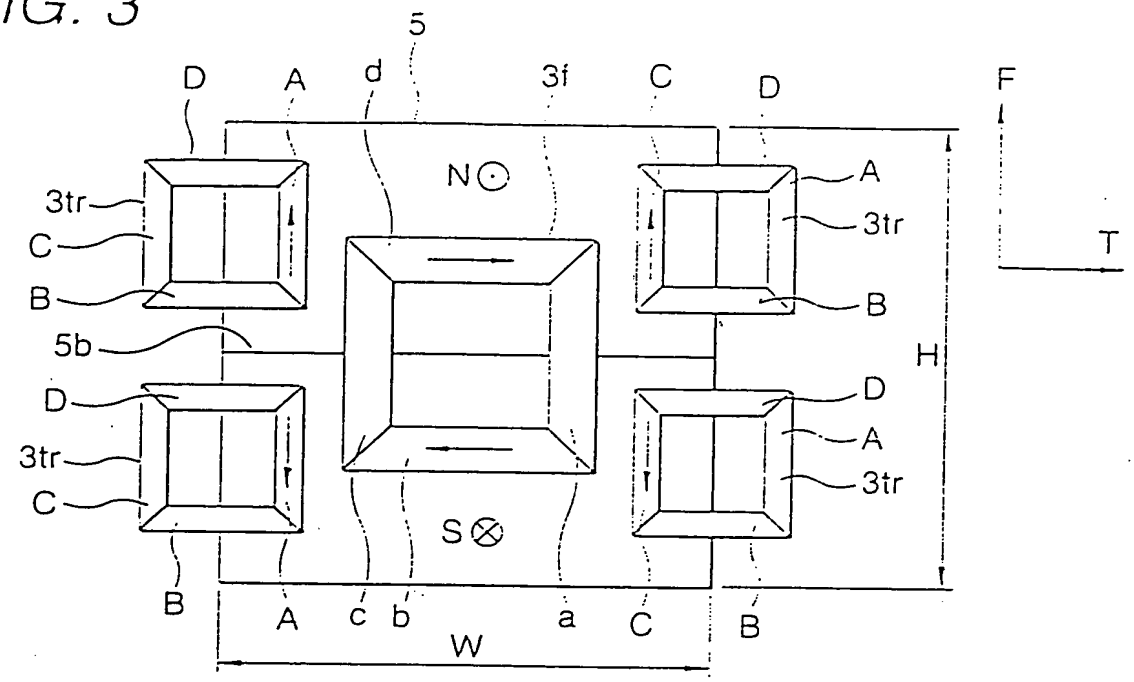

5
D
A
d
3i
C
D
F
N
A
3tr
T
3tr
C
B
B
5b
H
D
D
A
3tr
3tr
C
S
B
A
c
b
a
C
B
W

FIG. 4

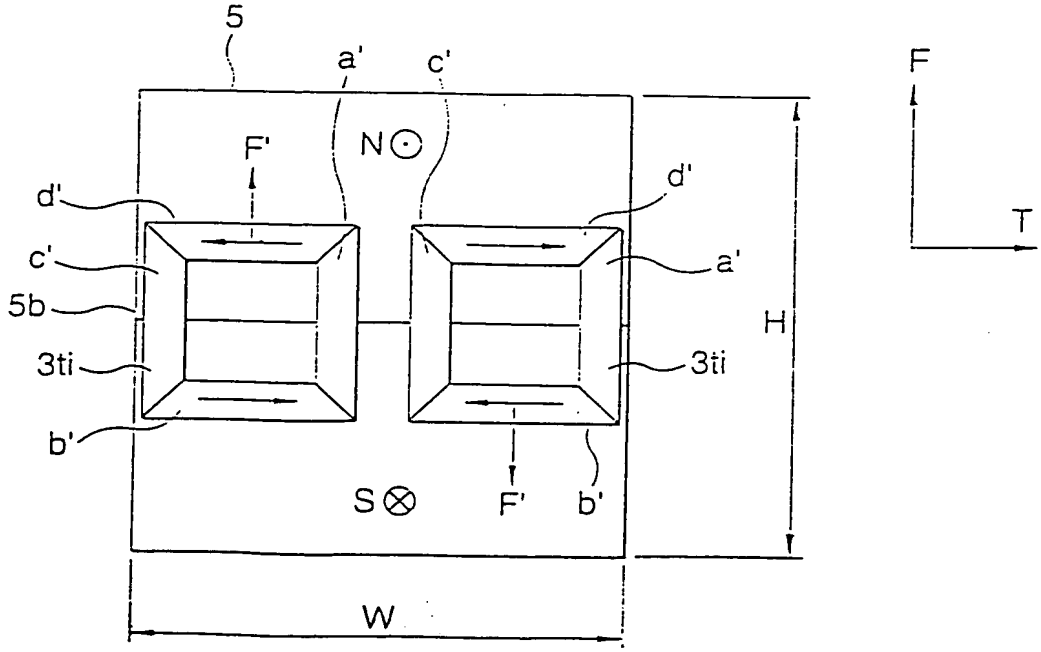

5
a'
c'
F
F'
N
d'
d'
T
c'
a'
5b
H
3ti
3ti
b'
S
F'
b'
W

FIG. 5

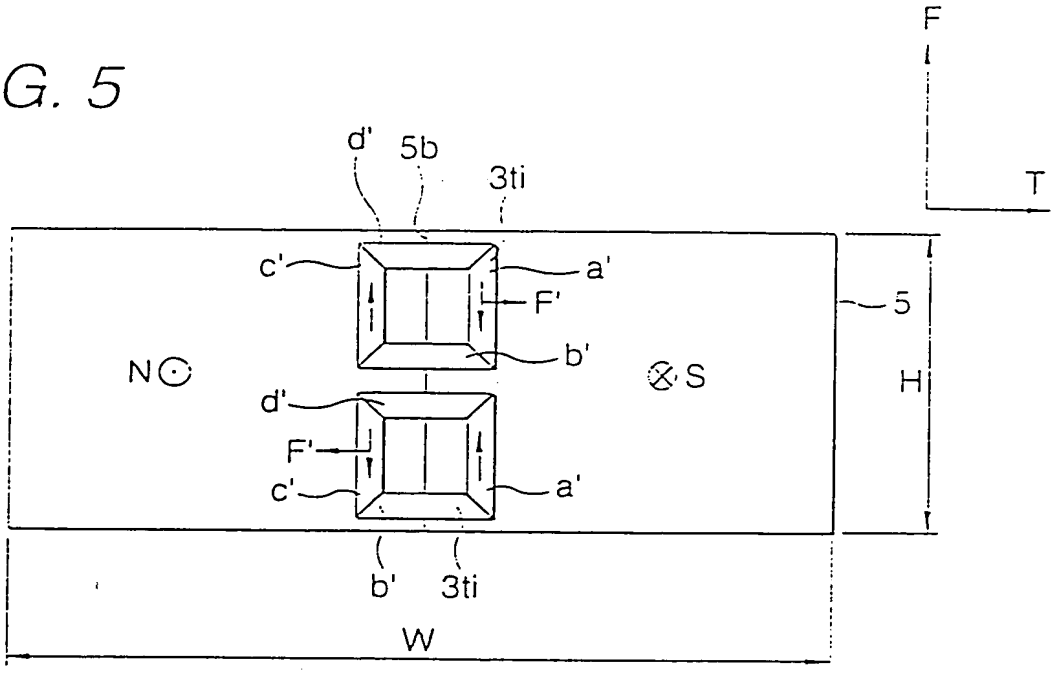


FIG. 6

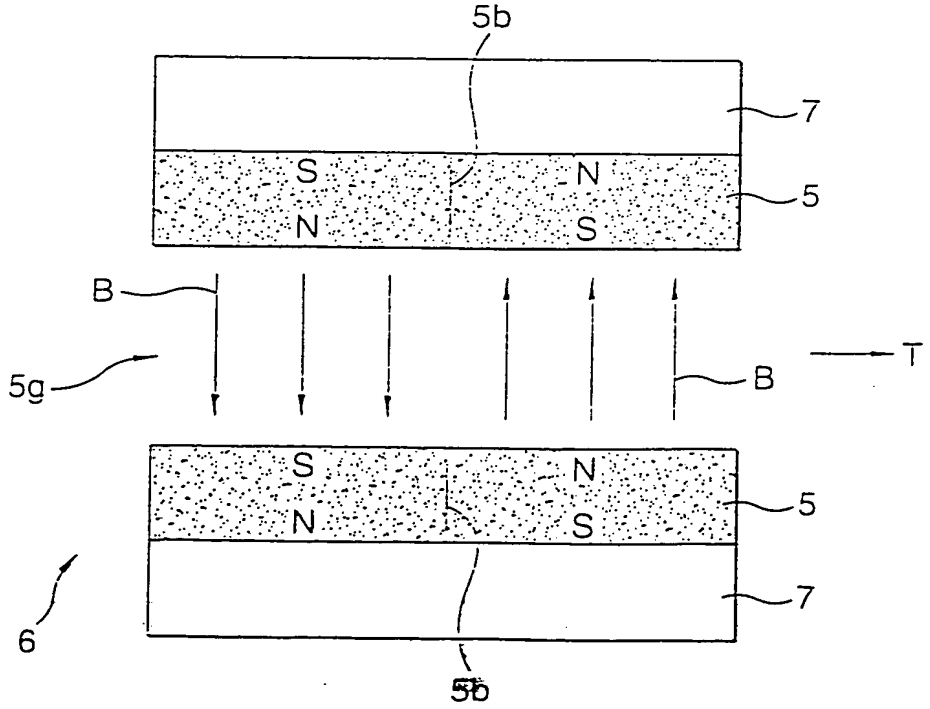


FIG. 7

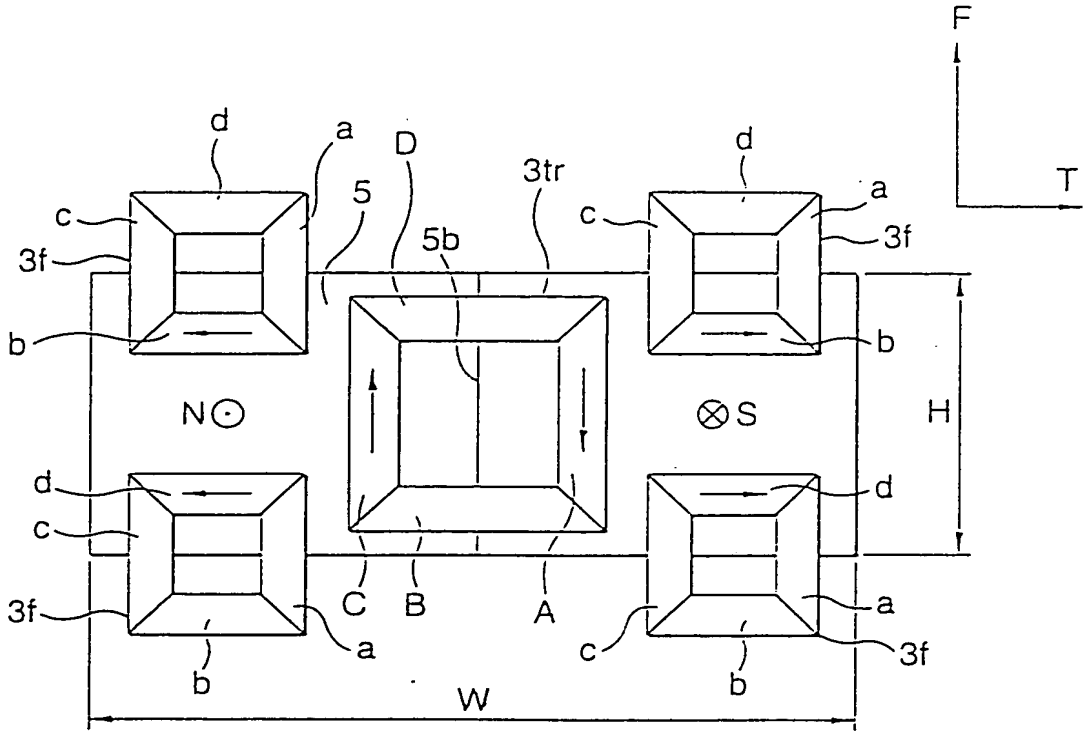


FIG. 8

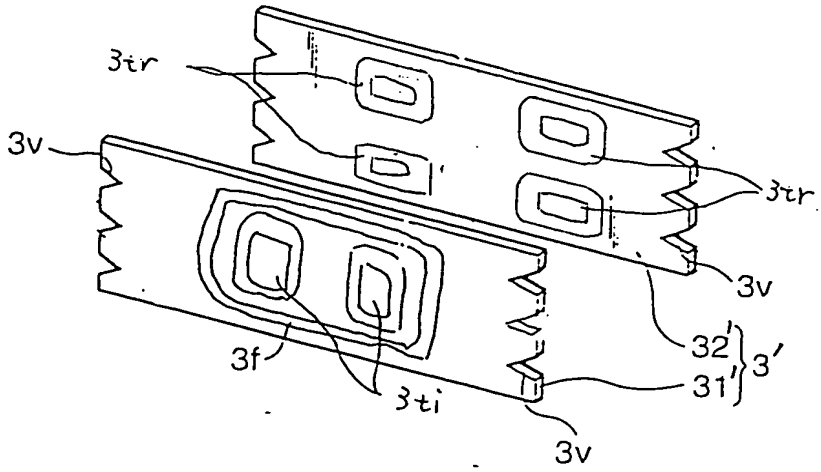


FIG. 9

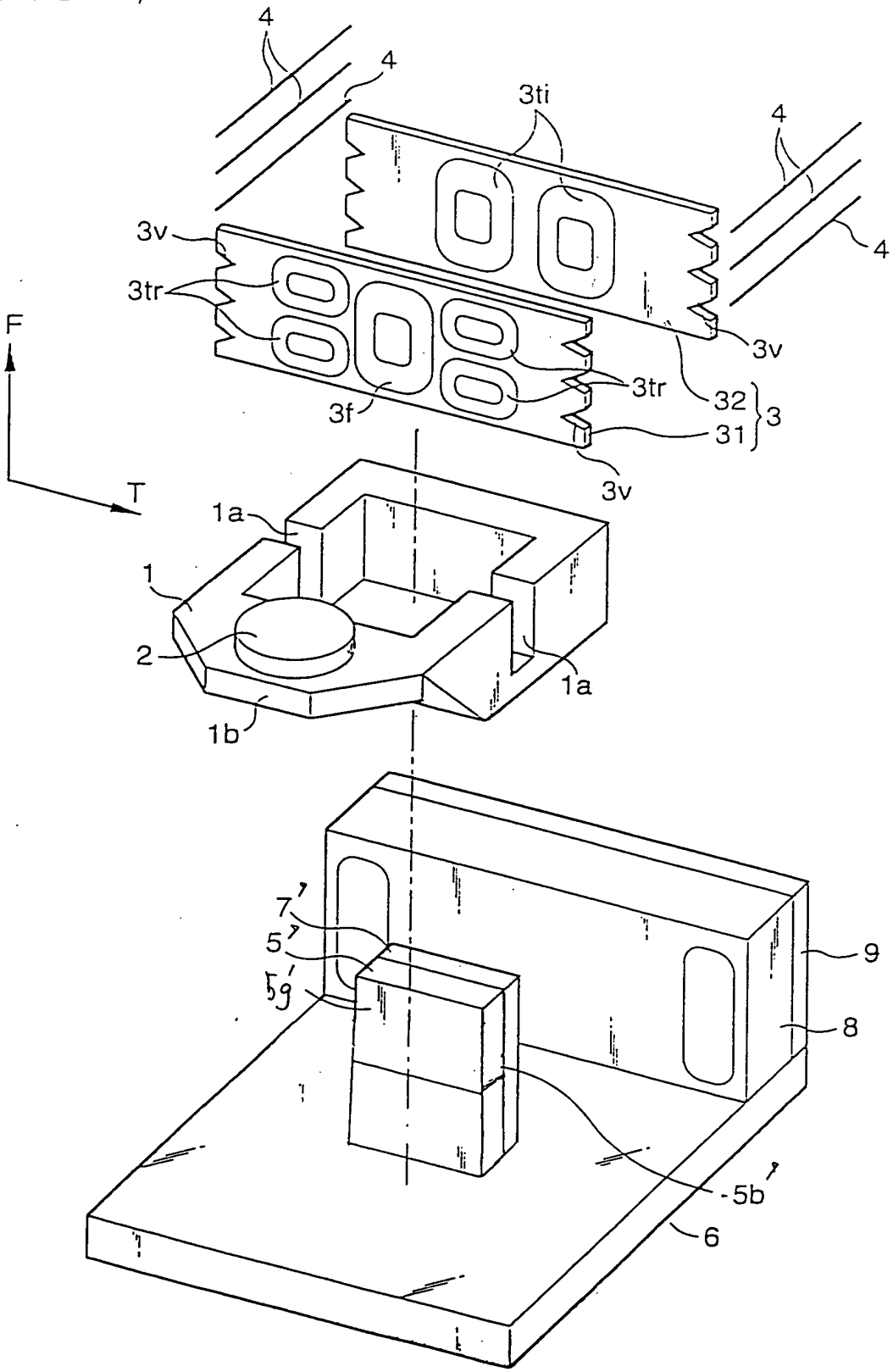


FIG. 10

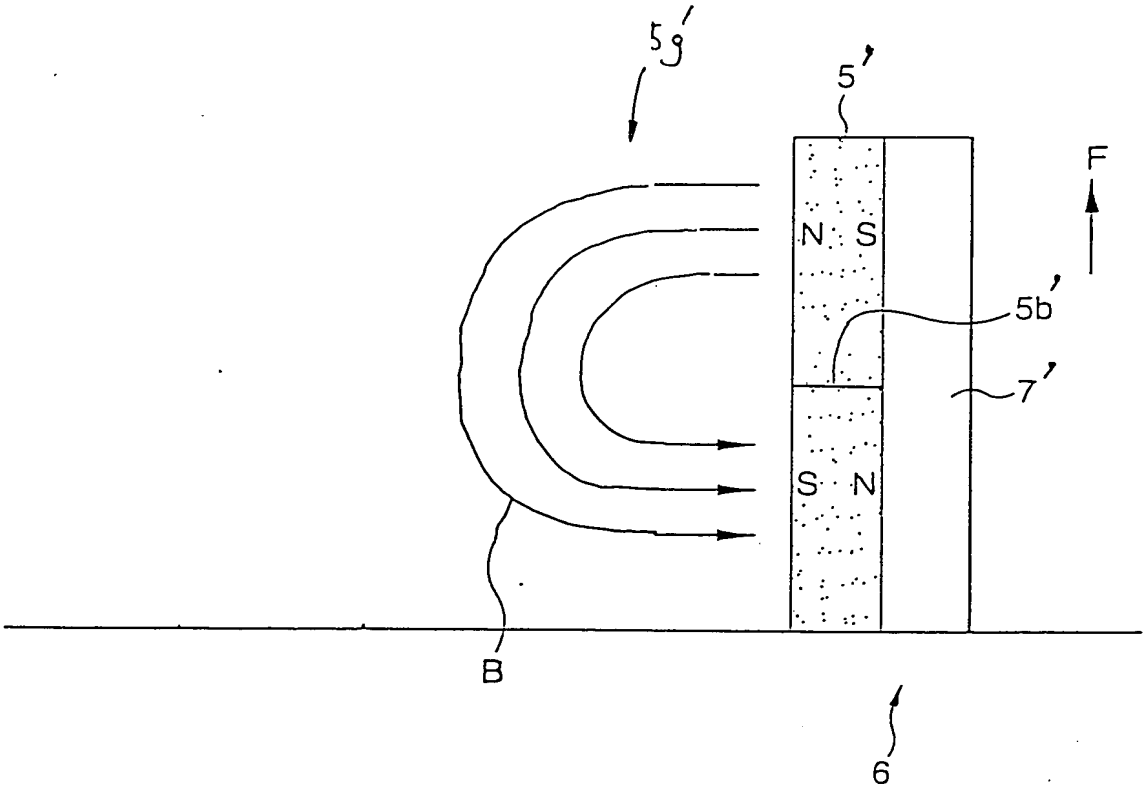


FIG. 11

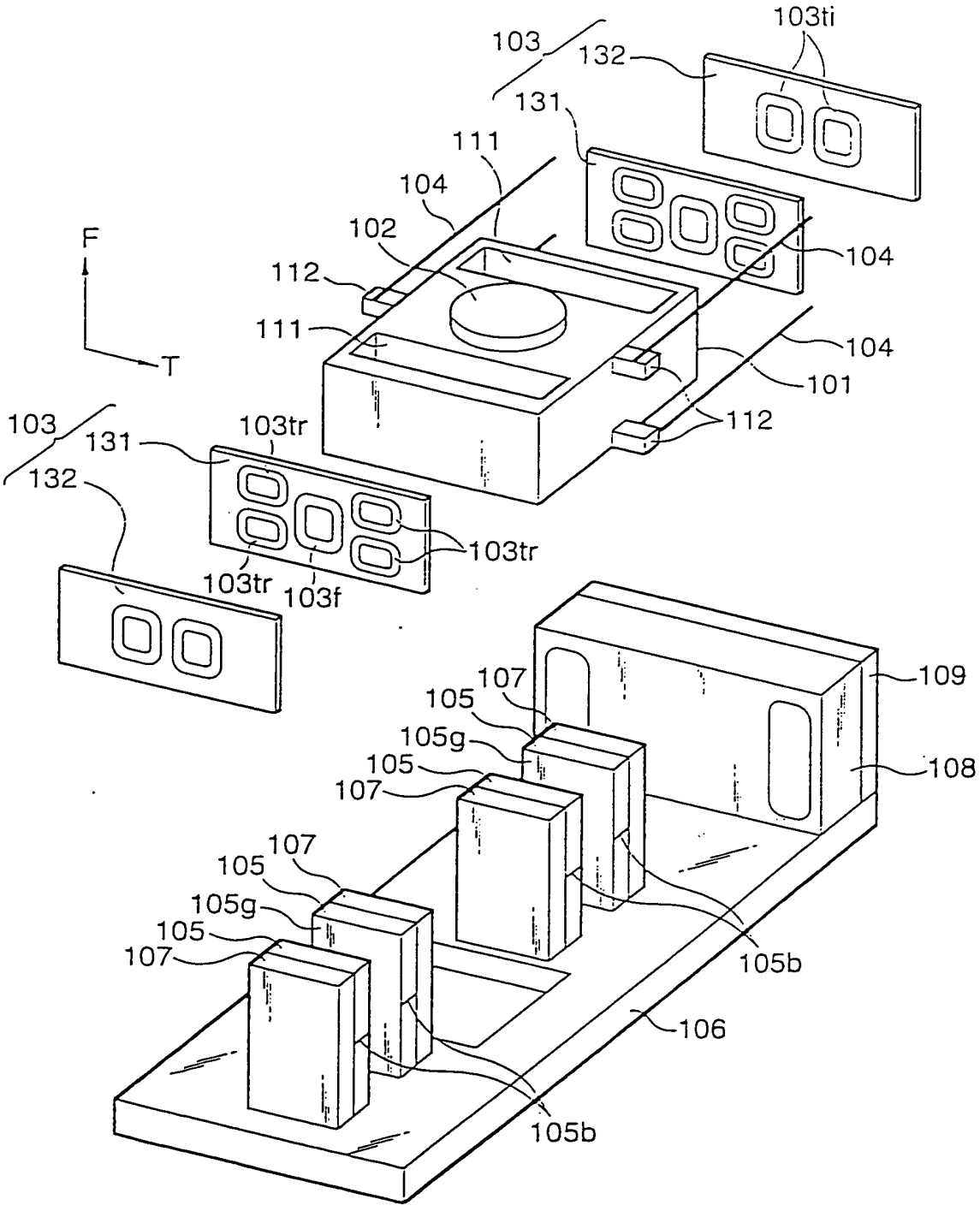

103ti
103
132
131
111
104
102
112
F
T
104
111
104
101
112
103
131
103tr
132
103tr
103tr
103f
109
107
105
105g
105
107
108
107
105
105g
105
107
105b
106
105b

FIG. 12

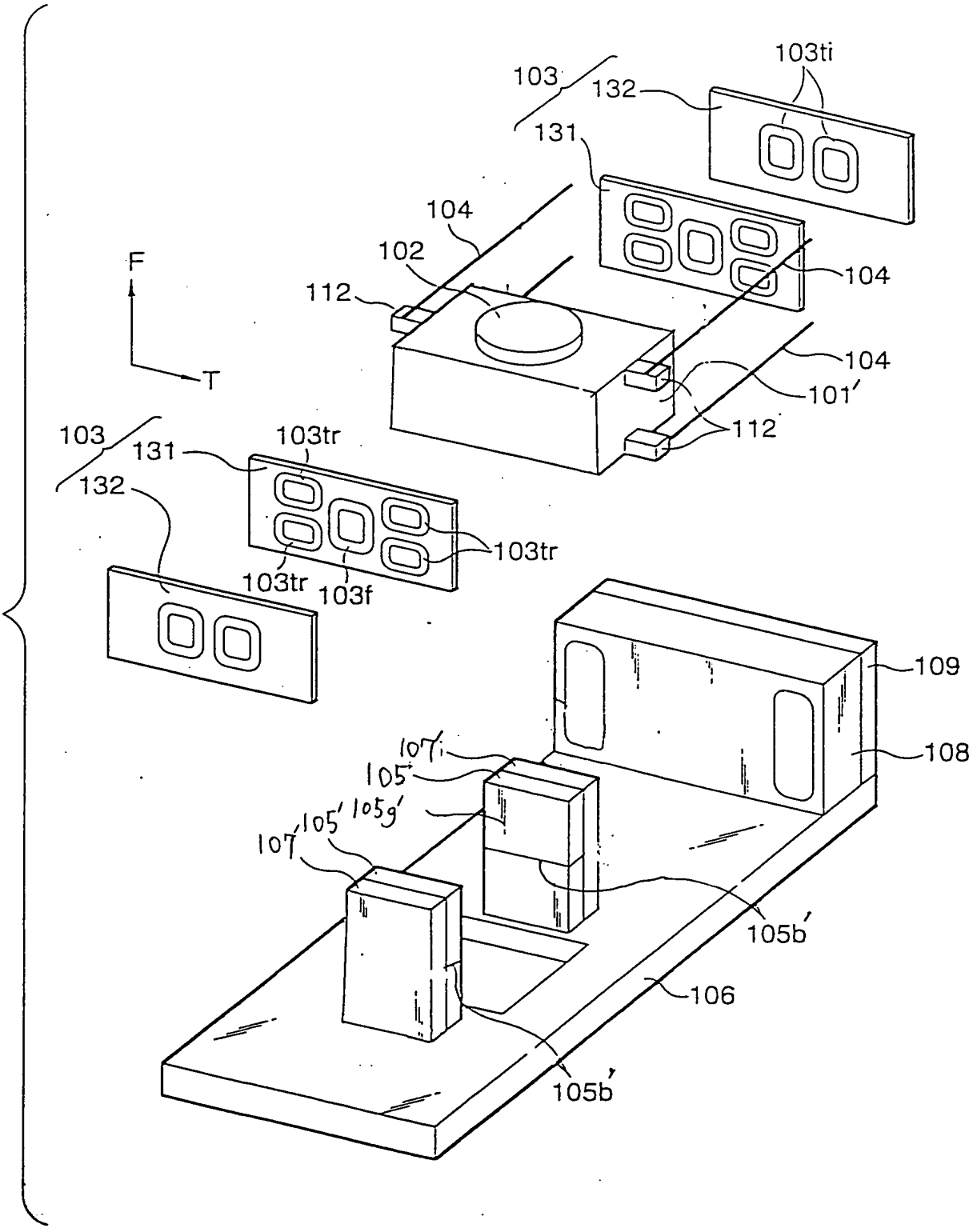

103ti
103
132
131
104
102
112
F
T
104
104
101'
112
103
131
103tr
132
103tr
103tr
103f
109
108
107i
105
105'
105g'
107
105b'
106
105b'

FIG. 13A

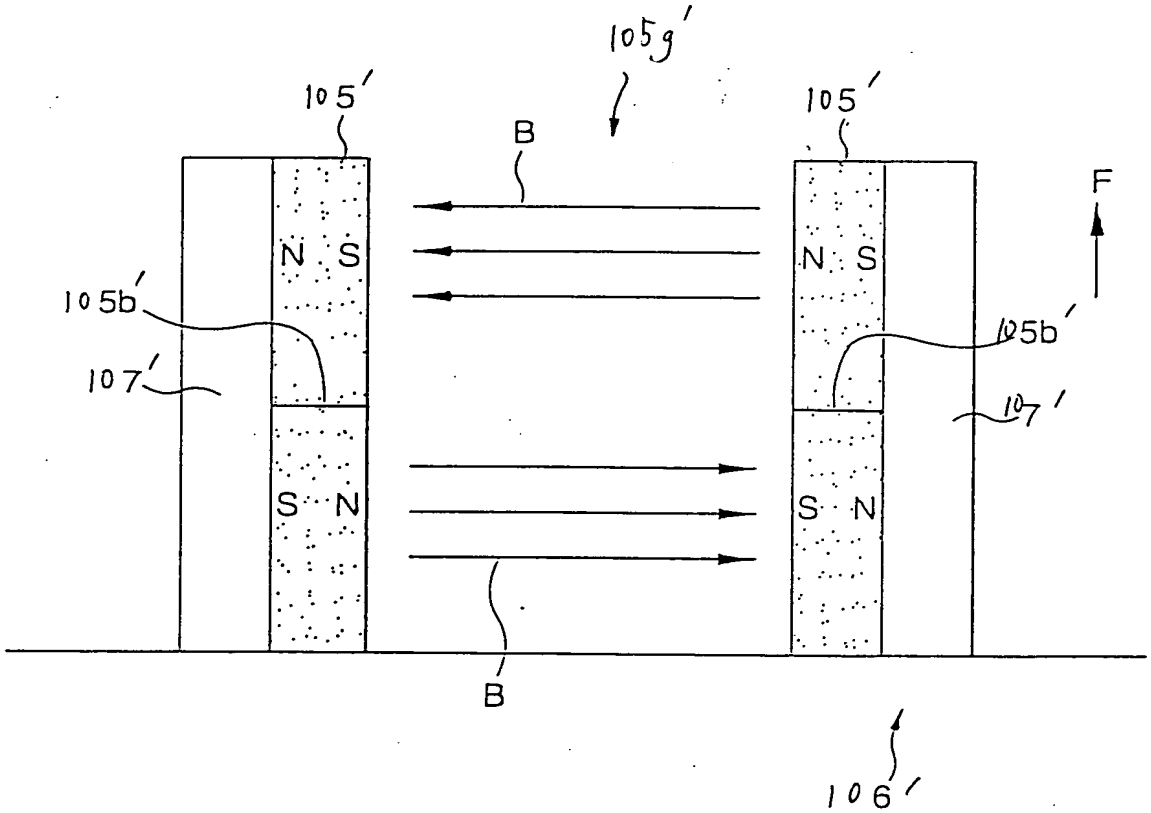


FIG. 13B

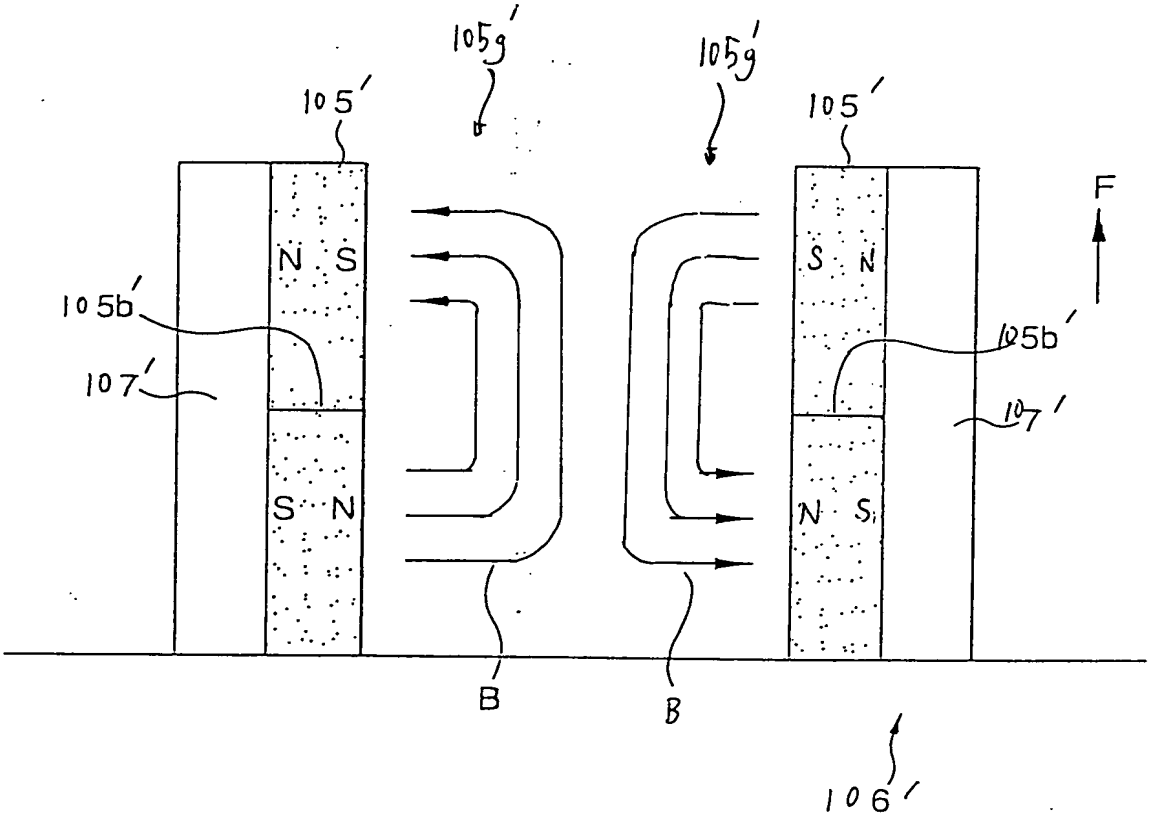


FIG. 14

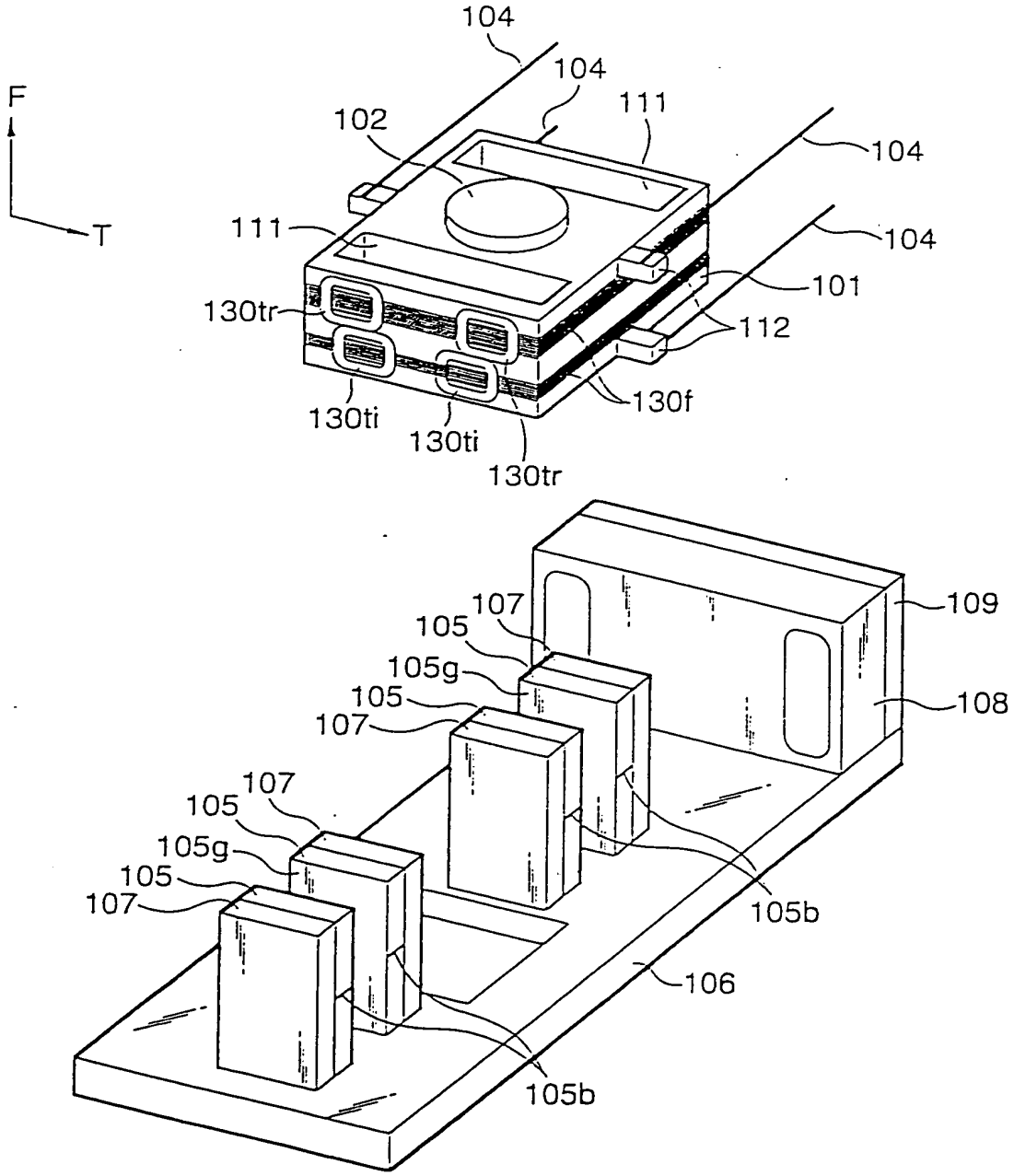

F
T
104
104
111
102
104
111
104
101
130tr
112
130f
130ti
130ti
130tr
109
107
105
105g
105
108
107
107
105
105g
105
107
105b
106
105b

FIG. 15

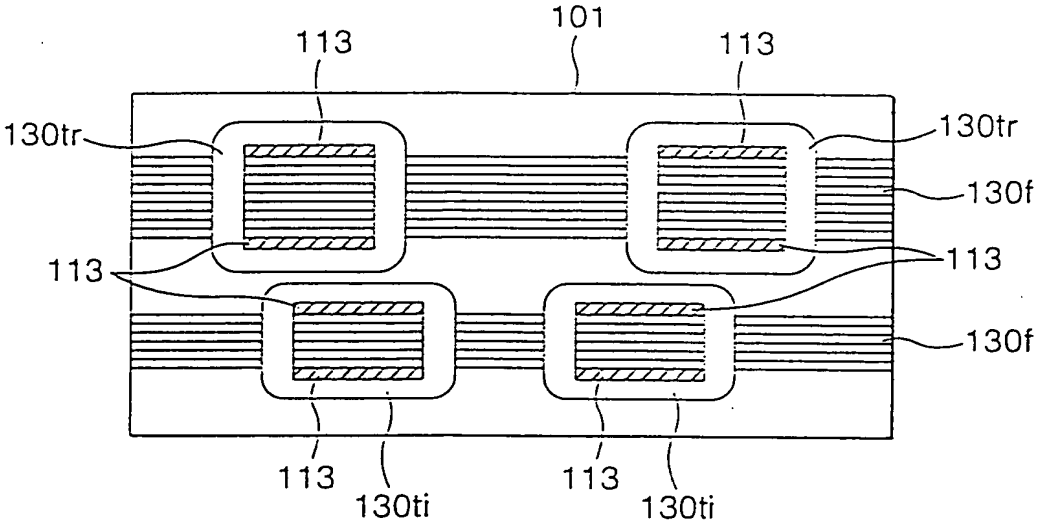

101
113
113
130tr
130tr
130f
113
113
130f
113
130ti
113
130ti

FIG. 16

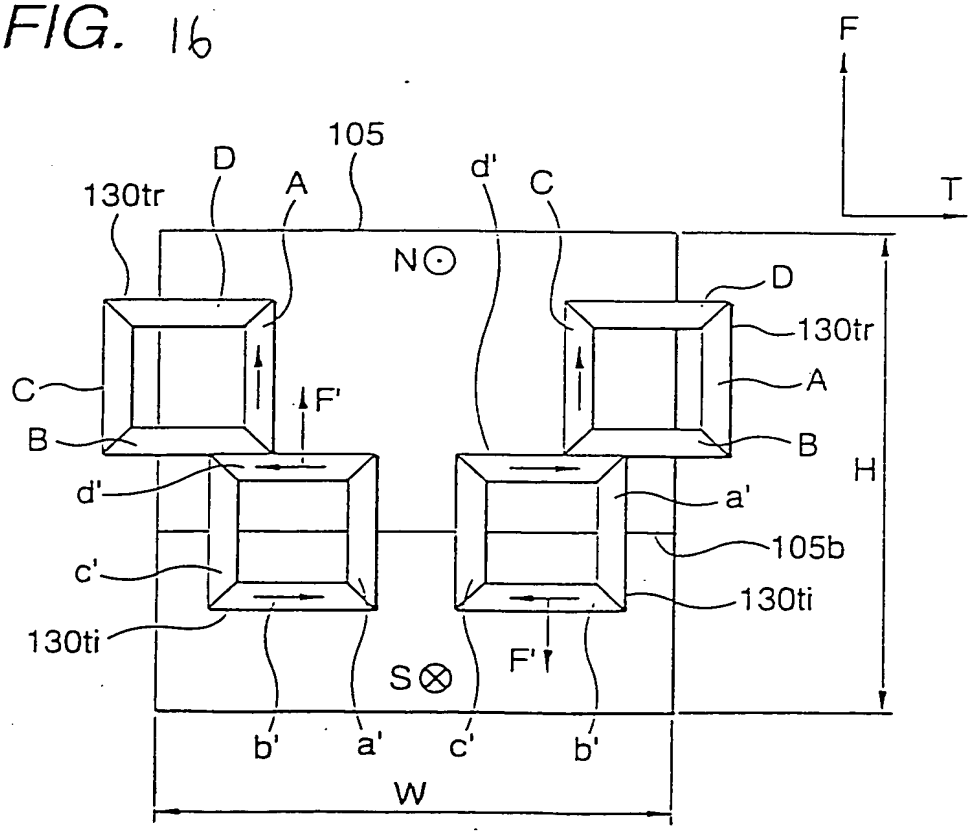

F
T
105
D
A
d'
C
130tr
N
D
130tr
C
A
F'
B
B
H
d'
a'
105b
c'
130ti
130ti
S
F'
b'
a'
c'
b'
W

FIG. 17

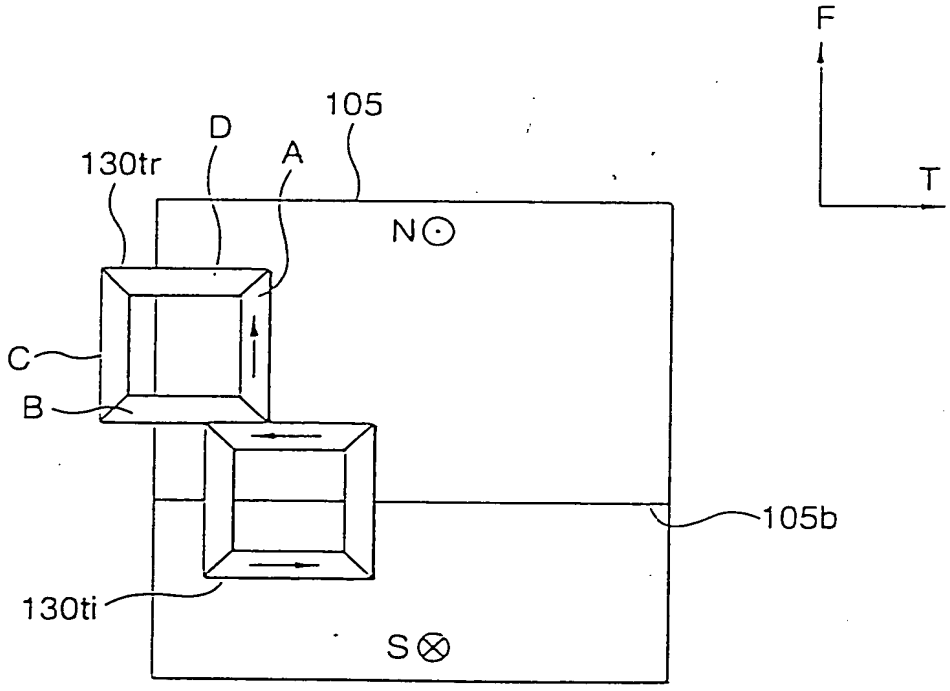


FIG. 18

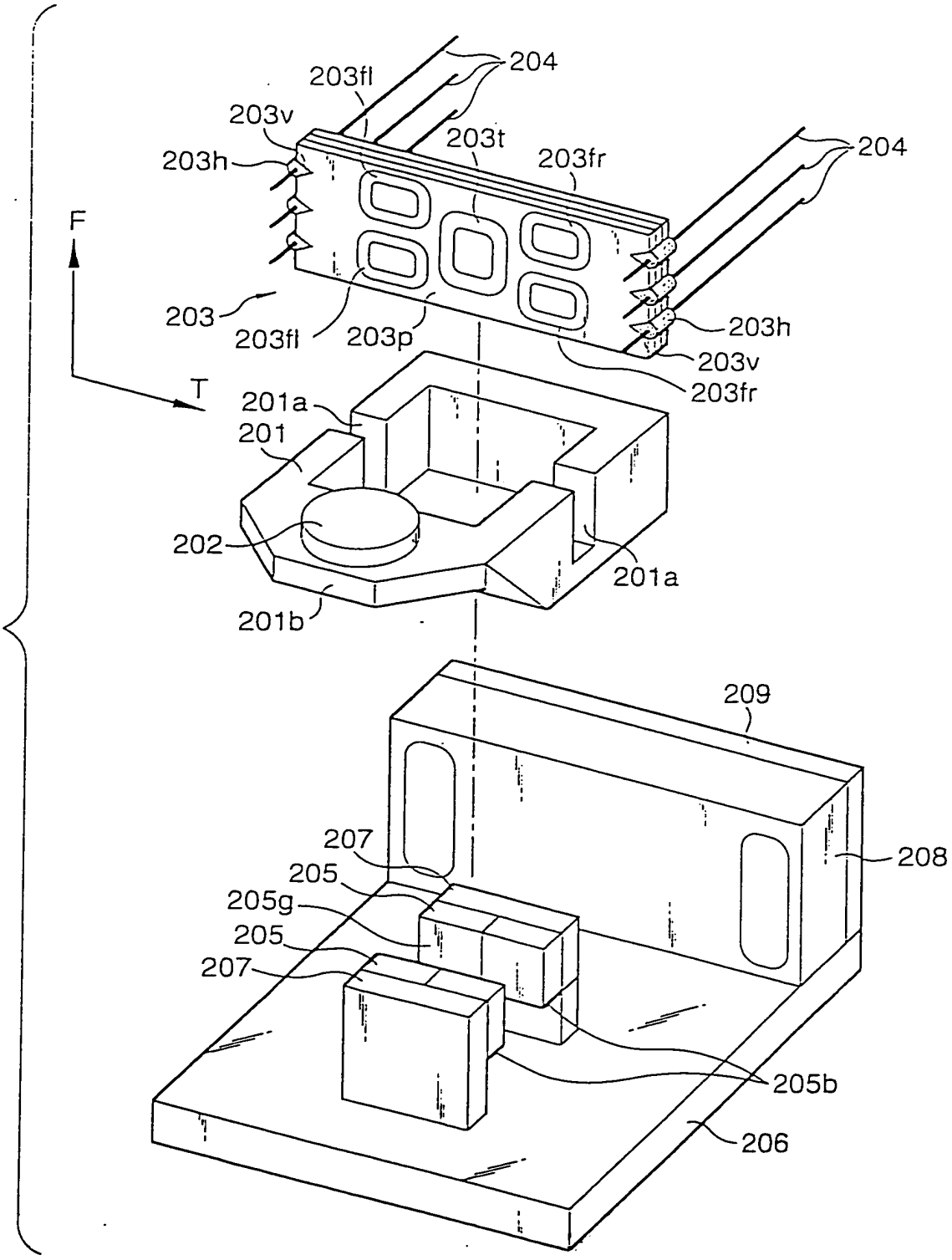

204
203fl
203v
203t
203fr
204
203h
F
203h
203
203fl
203p
203v
T
203fr
201a
201
202
201a
201b
209
207
208
205
205g
205
207
205b
206

FIG. 19

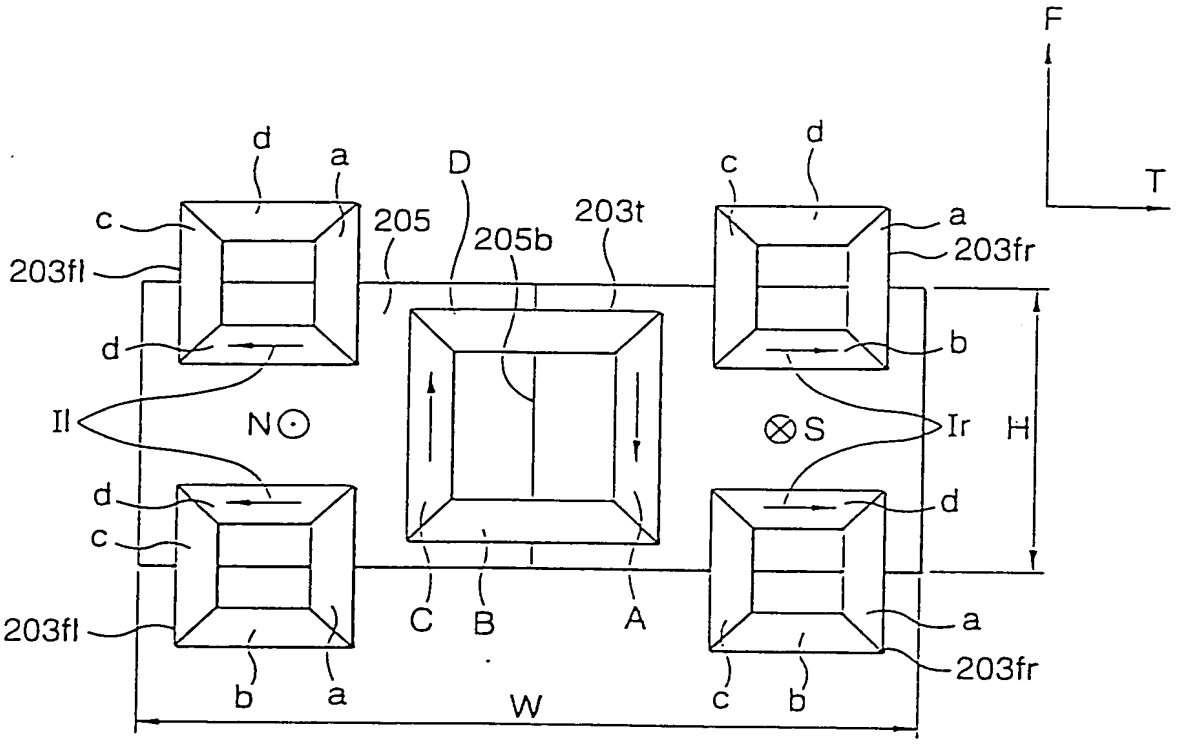

F
T
d
a
D
c
d
c
205
205b
203t
a
203fr
203fl
d
b
Il
N⊙
⊗S
Ir
H
d
d
c
203fl
C
B
A
a
203fr
b
a
W
c
b

FIG. 20

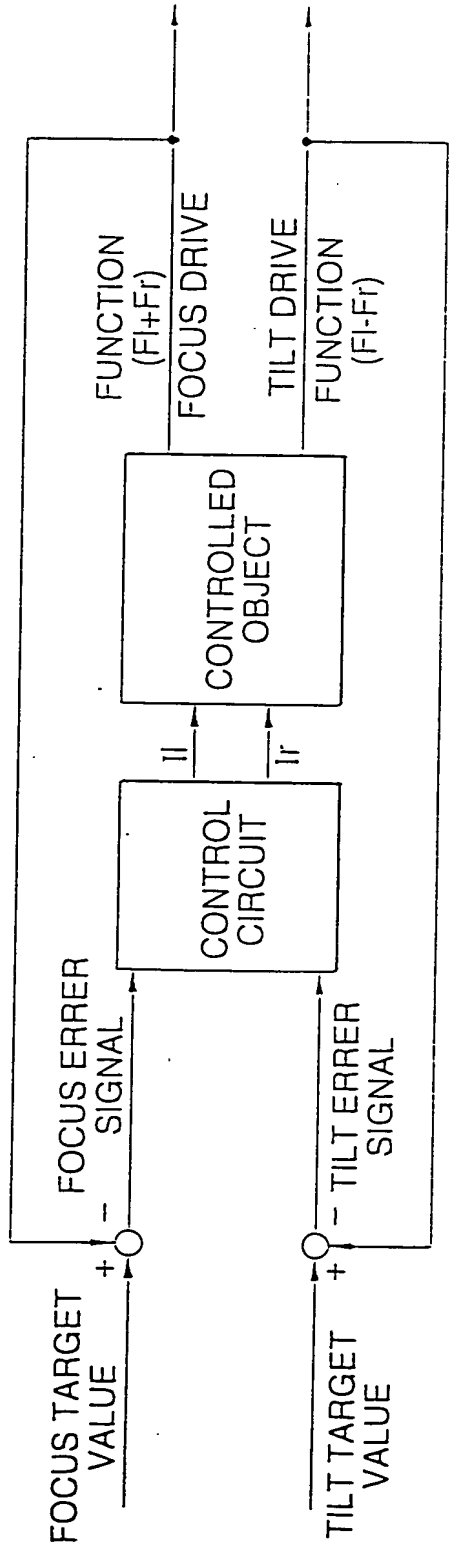

FOCUS TARGET VALUE
TILT TARGET VALUE
FOCUS ERRER SIGNAL
TILT ERRER SIGNAL
CONTROL CIRCUIT
Il
Ir
CONTROLLED OBJECT
FUNCTION (Fl+Fr)
FOCUS DRIVE
TILT DRIVE
FUNCTION (Fl-Fr)

FIG. 21A

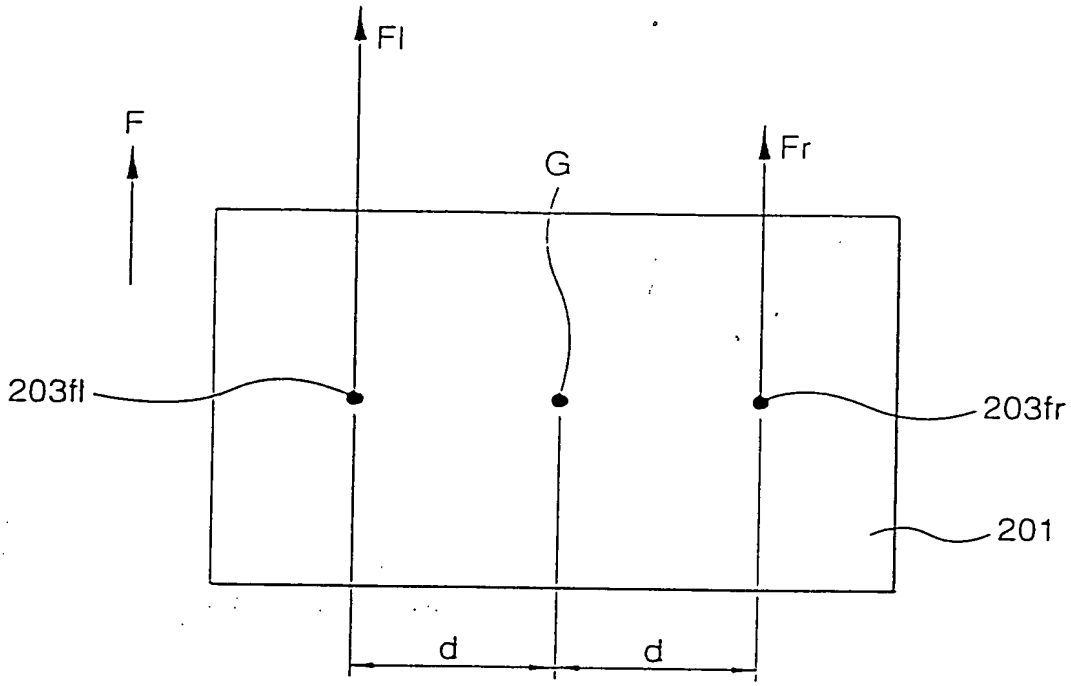


FIG. 21B

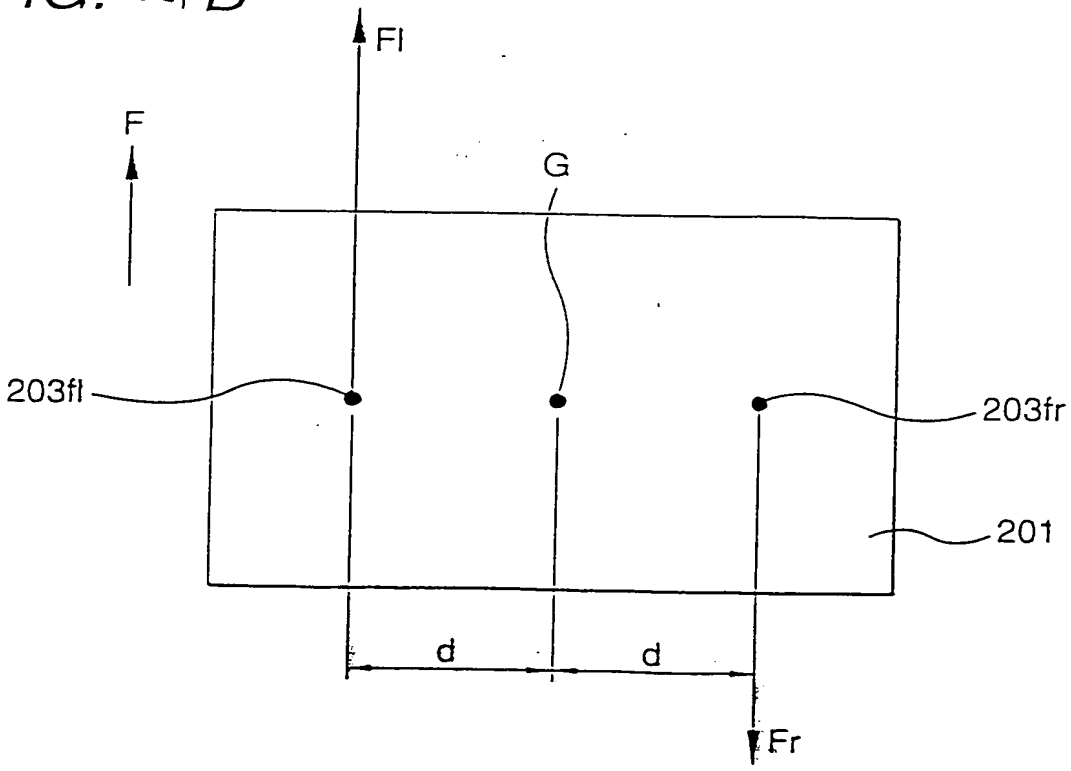


FIG. 22

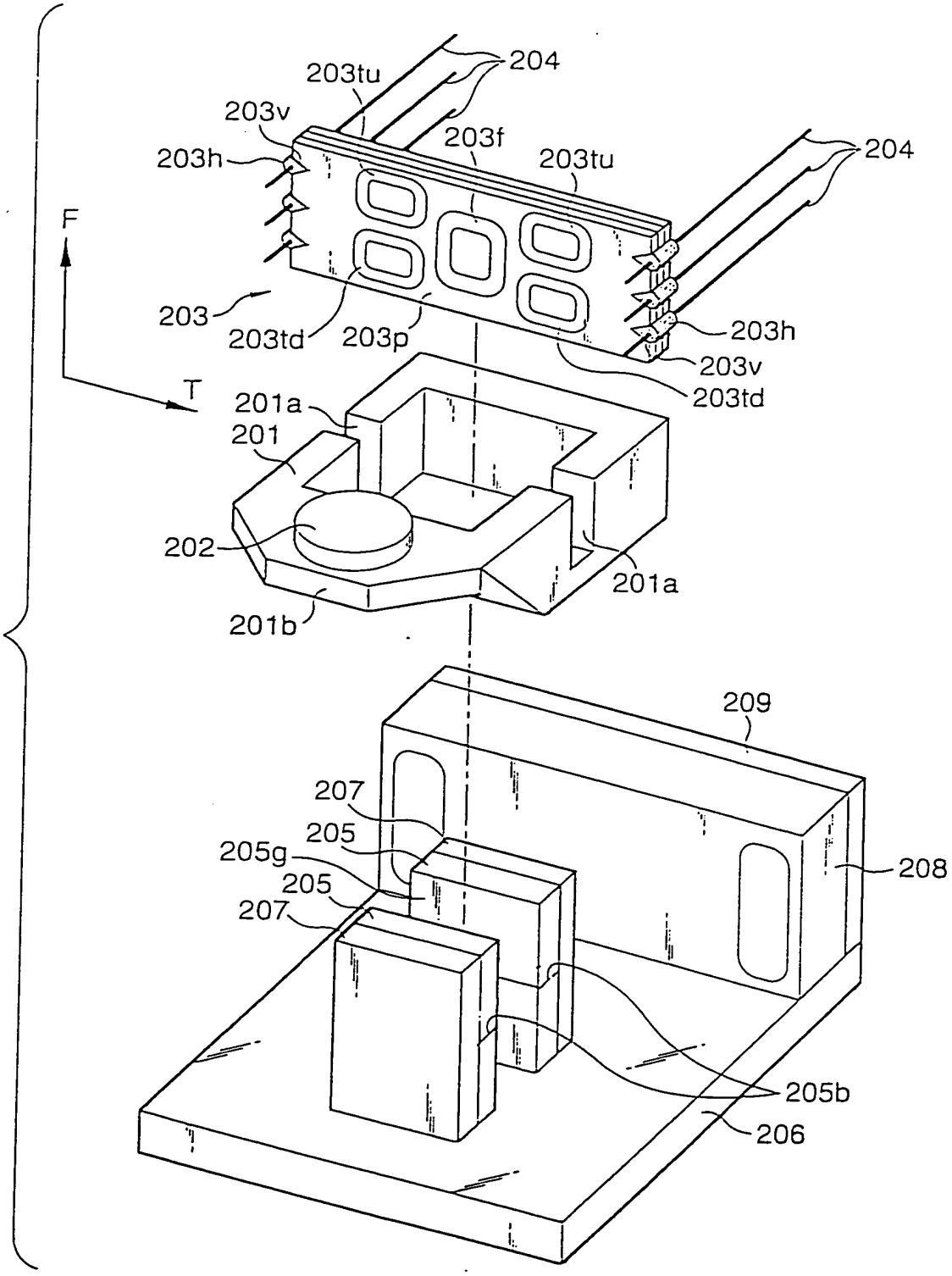

203tu
204
203v
203f
203h
203tu
204
F
203
203td
203p
203h
203v
T
203td
201a
201
202
201a
201b
209
207
205
205g
205
207
208
205b
206

FIG. 23

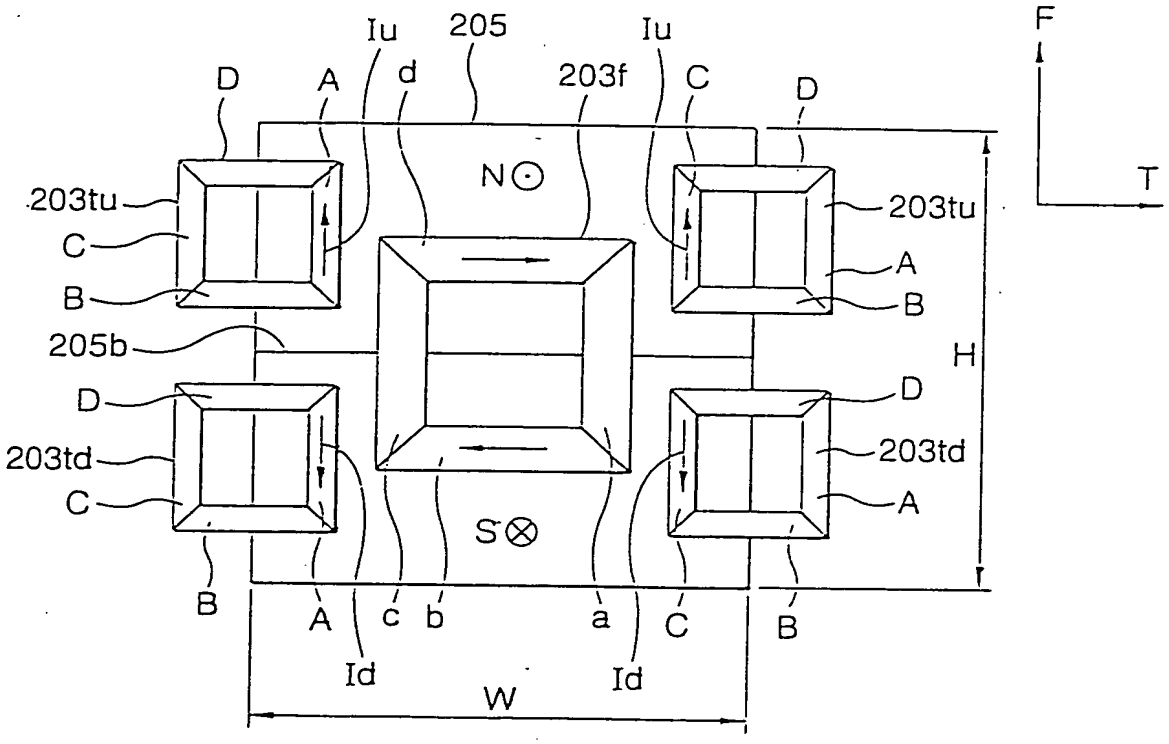

Iu
205
Iu
F
D
A
d
203f
C
D
T
203tu
N
203tu
C
A
B
B
205b
H
D
D
203td
203td
C
A
S
B
A
c
b
a
C
B
Id
Id
W

FIG. 24

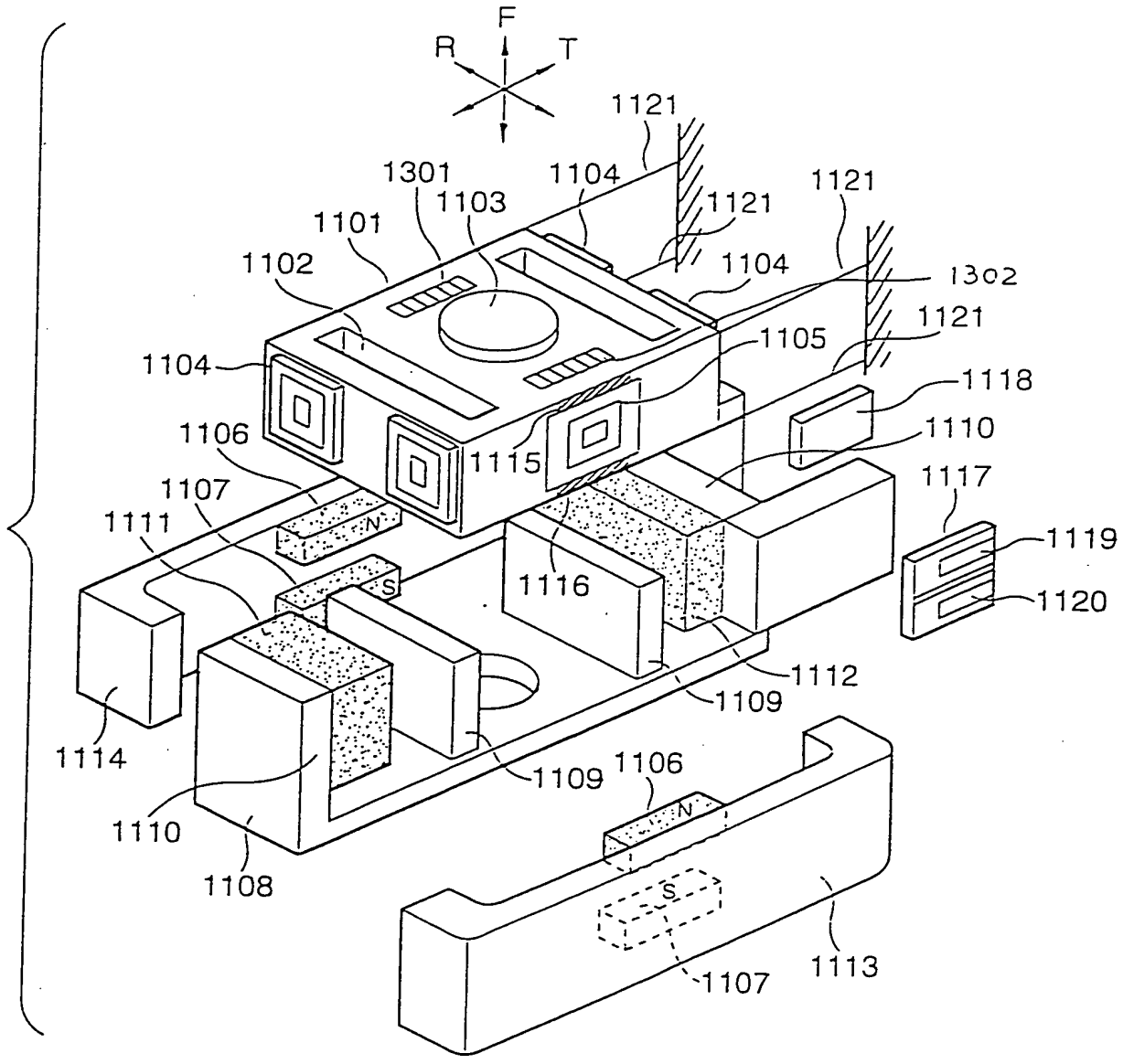

F
R
T
1121
1301
1104
1103
1121
1121
1101
1102
1104
1302
1121
1105
1104
1118
1106
1115
1110
1107
1117
1111
N
1119
S
1116
1120
1112
1109
1114
1109
1106
1110
N
1108
S
1113
1107

FIG. 25

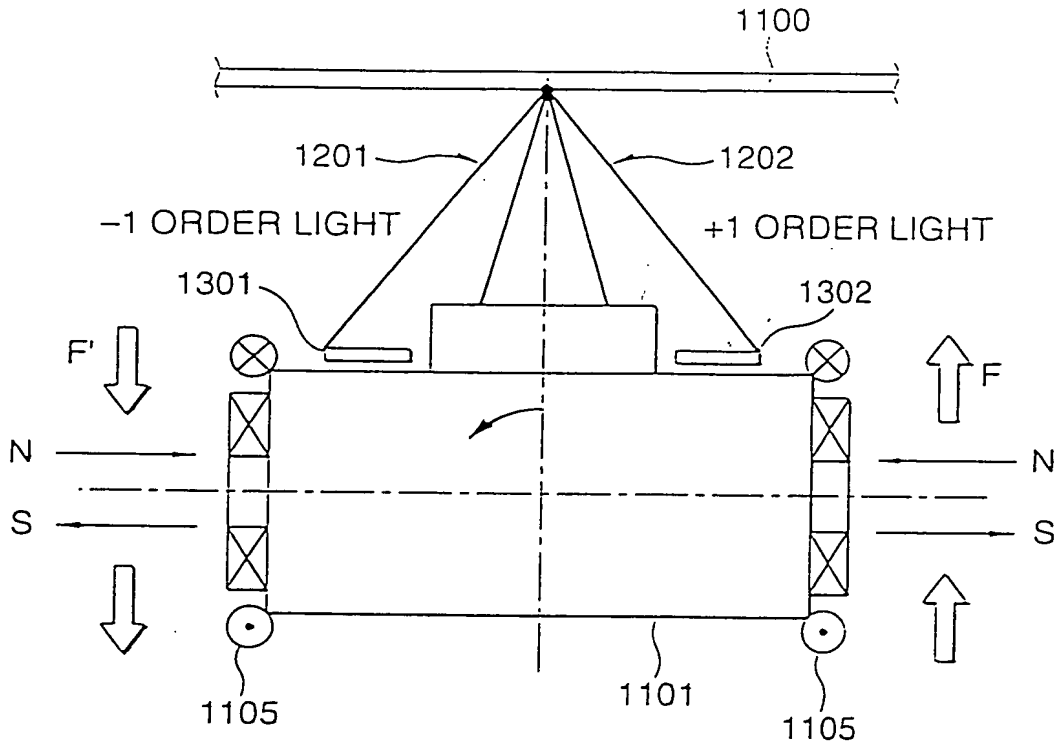

1100
1201
1202
−1 ORDER LIGHT
+1 ORDER LIGHT
1301
1302
F'
F
N
S
N
S
1105
1101
1105

FIG. 26

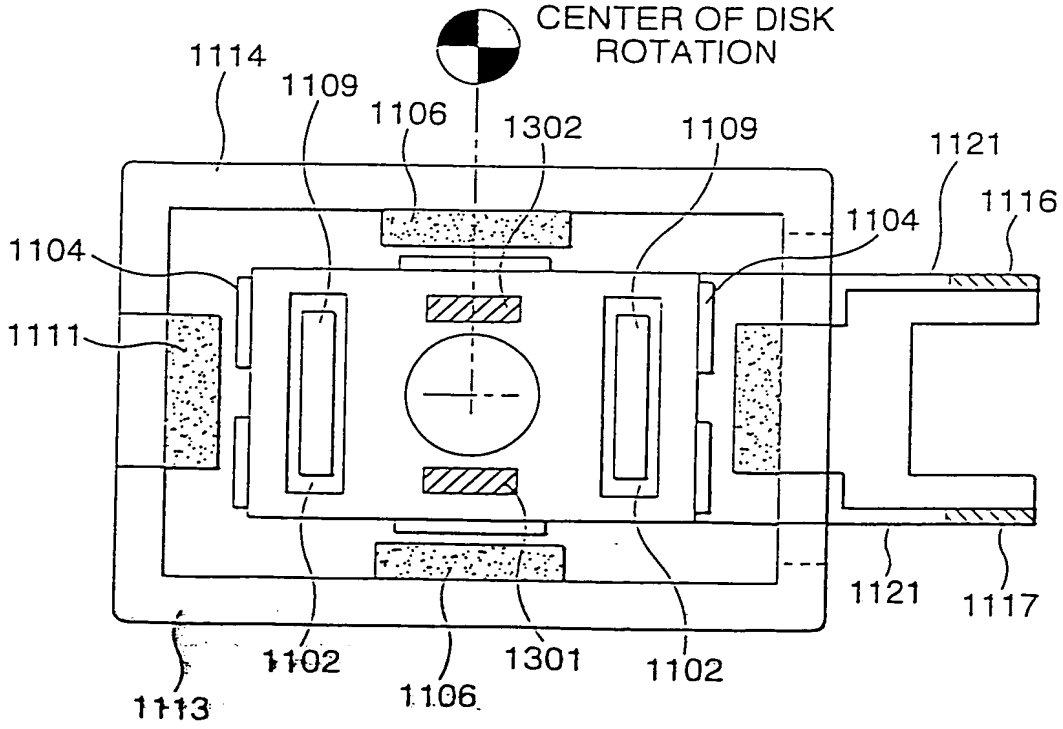
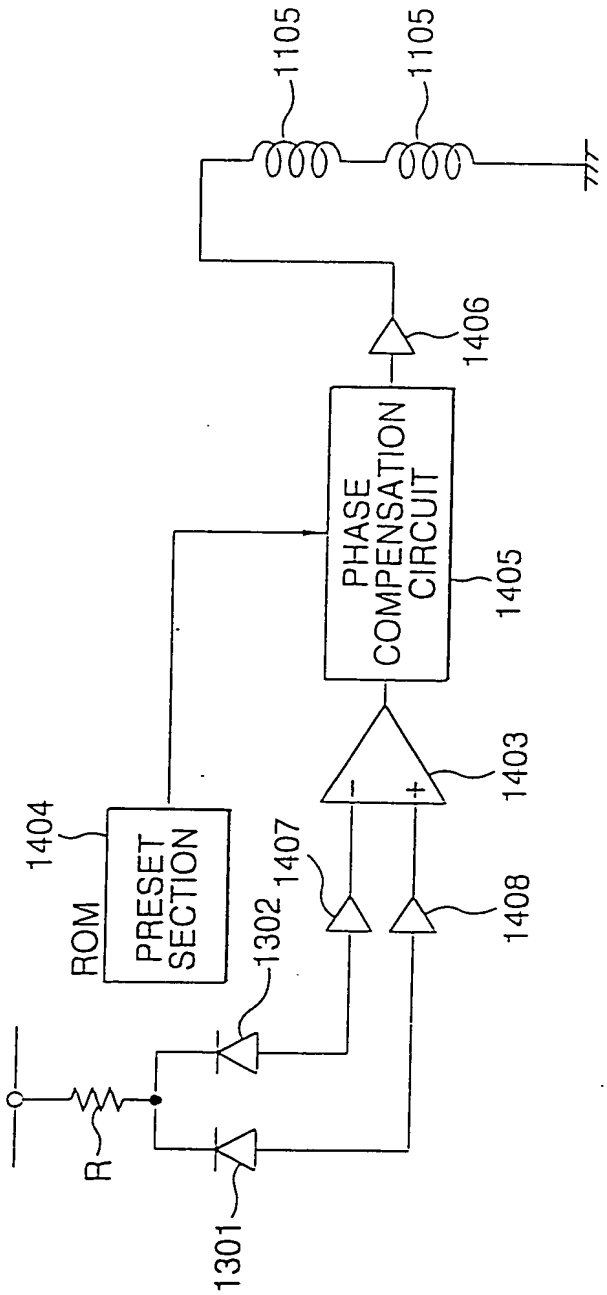

CENTER OF DISK ROTATION
1114
1109
1106
1302
1109
1121
1116
1104
1104
1111
1121
1117
1102
1301
1102
1113
1106

FIG. 27


1105
1105
1406
PHASE COMPENSATION CIRCUIT
1405
1403
1404
ROM
PRESET SECTION
1407
1408
1302
R
1301